

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3058	("JOHNSON, RICHARD" or "MORGAN, ANDREW" or "ANVIN, H. PETER" or "TORVALDS, LINUS")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:36
L2	5	l1 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:37
L3	477	"TRANSMETA CORPORATION"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:37
L4	7	l3 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:37
L5	12222	((("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:38
L6	261	l5 and ("memory" adj "architecture")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:38
L7	0	l6 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:40
L8	69	((("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:40
L9	4	l8 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:40
L10	2545	713/189	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:47
L11	50	l10 and ("memory" adj "architecture")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:48
L12	15	l11 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:48

L13	0	l12 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:48
L14	3041	711/147	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:48
L15	260	l14 and ("memory" adj "architecture")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:49
L16	7	l15 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:49
L17	0	l16 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:49
L18	3	l14 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:49
L23	5326	711/170	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:54
L24	313	l23 and ("memory" adj "architecture")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:54
L25	25	l24 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:55
L26	0	l25 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:55
L27	3	l23 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:55
L28	2424	711/173	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:56
L29	125	l28 and ("memory" adj "architecture")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:56
L30	3	l29 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:56

L31	0	l30 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:57
L32	1	l28 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 19:57
L33	415	710/13	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:01
L34	8	l33 and ("memory" adj "architecture")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:01
L35	0	l34 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:01
L36	0	l34 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:01
L37	0	l33 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:02
L38	3556	709/213	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:03
L39	109	l38 and ("memory" adj "architecture")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:03
L40	3	l39 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:03
L41	0	l40 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:03
L42	2	l38 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:03
L43	1284	709/214	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:05
L44	56	l43 and ("memory" adj "architecture")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:05

L45	1	l44 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space"))))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:05
L46	0	l45 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:05
L47	0	l43 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:06
L48	968	709/215	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:10
L49	84	l48 and ("memory" adj "architecture")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:10
L50	18	l49 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:10
L51	0	l50 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:10
L52	0	l48 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:10
L53	1681	709/216	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:10
L54	51	l53 and ("memory" adj "architecture")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:11
L55	1	l54 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:11
L56	2	l53 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:11
L57	295	712/14	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:13
L58	27	l57 and ("memory" adj "architecture")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:13

L59	0	I58 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:13
L60	0	I58 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:14
L61	0	I57 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:14
L62	174	712/27	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:14
L63	10	I62 and ("memory" adj "architecture")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:14
L64	0	I63 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:15
L65	0	I63 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:15
L66	0	I58 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:15
L67	13004	707/100	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:17
L68	55	I67 and ("memory" adj "architecture")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:17
L69	3	I68 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:17
L70	0	I69 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:17
L71	0	I68 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:18

L72	0	l67 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/09/25 20:18
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